

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Hiroyuki Nakajima	Examiner:	Brian P. Johnson
Serial No.:	10/785,671	Art Unit:	2183
Filed:	February 24, 2004	Docket:	17472
For:	PROCESSOR, SYSTEM LSI CIRCUIT, METHOD OF DESIGNING SAME, AND RECORDING MEDIUM HAVING SAID METHOD RECORDED THEREON	Dated:	November 7, 2007

Confirmation No.: 2687

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE UNDER 37 C.F.R. §1.114

Sir:

This paper is responsive to the Final Rejection mailed August 8, 2007 in the above-captioned application. Favorable reconsideration and withdrawal of all rejections is kindly requested in light of the following amendments and accompanying remarks. Please amend the Application as follows.

CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this correspondence is being deposited with the United States Patent & Trademark Office via Electronic Filing through the United States Patent and Trademark Office e-business website, on the date below.

Dated: November 7, 2007


Katherine R. Vieyra